

**IN THE CLAIMS:**

**Kindly replace the claims of record with the following full set of claims:**

1. (Original) A data processing device comprising a bus system across which a transmitter circuit can transfer a series of samples to a receiver circuit, characterized in that:

the transmitter circuit is arranged for transmitting only a start of the series of samples which extends to the point where the samples that remain are equal to a reference value and for transmitting an indication "end of transmission" after the last transmitted sample;

the receiver circuit is arranged for placing reference values after the sample preceding the indication "end of transmission," so as to reconstitute the series of samples.

2. (Original) A method of transferring a series of samples from a transmitter circuit to a receiver circuit, characterized in that the method comprise the following steps;

a transmission step in which the transmitter circuit transmits only a start of the series of samples which extends to the point where the samples that remain are equal to a reference value and for transmitting an indication "end of transmission" after the last transmitted sample;

a receiving step in which the receiver circuit places reference values after the sample preceding the indication "end of transmission," so as to reconstitute the series of samples.